

QSFP28 50GBASE-BR40

Databrief

Product Features

- QSFP28 MSA compliant
- Hot pluggable 38 pin electrical interface
- 1 x 50G PAM4 transmitter
- 26.5625 Gbps Channel Electrical Serial Interface (50GAUI-2)
- Maximum power consumption 4.5 W
- Single LC duplex connector
- Supports 53.125 Gbps aggregate bit rate
- Up to 40 km transmission on single mode fiber
- Operating case temperature: 0°C to 70°C
- Single 3.3 V power supply
- RoHS 2 compliant

Applications

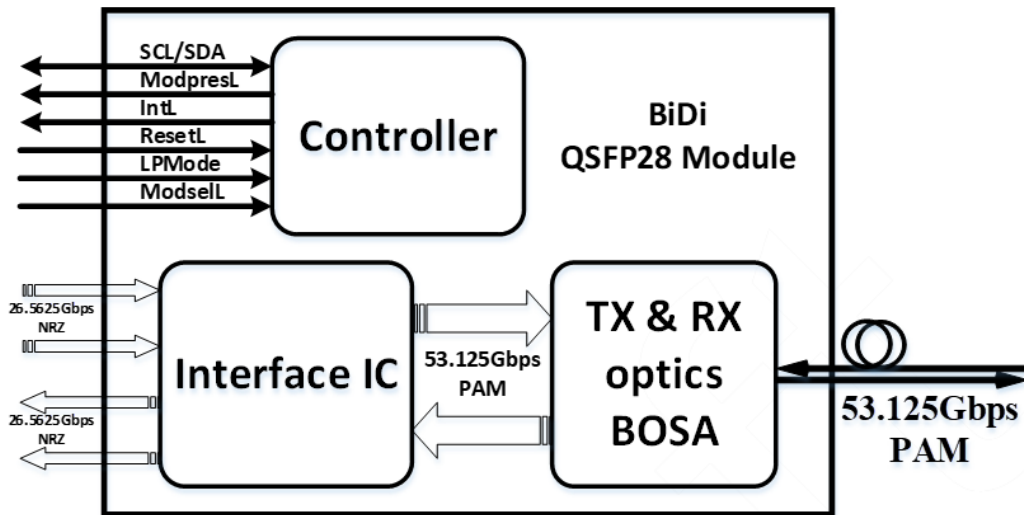
- 50GBASE-BR40
- Telecom networking



1. Function Descriptions

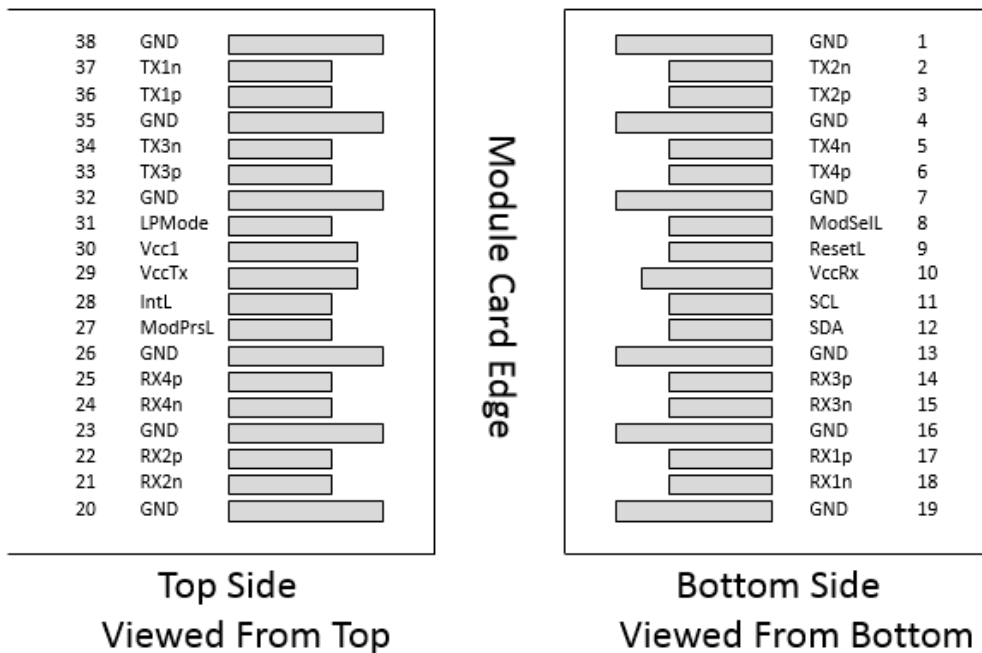
OM9382ES101&OM9382ES102 QSFP28 transceiver module is designed for use in 50 Gigabit Ethernet links on up to 40 km of single mode fiber. It is compliant with the QSFP28 MSA (SFF-8679, etc). Digital diagnostic functions are available via the I2C interface, as specified by the MSA. A block diagram is shown in Figure 1-1.

Figure 1-1 Transceiver block diagram



2. Pin Descriptions

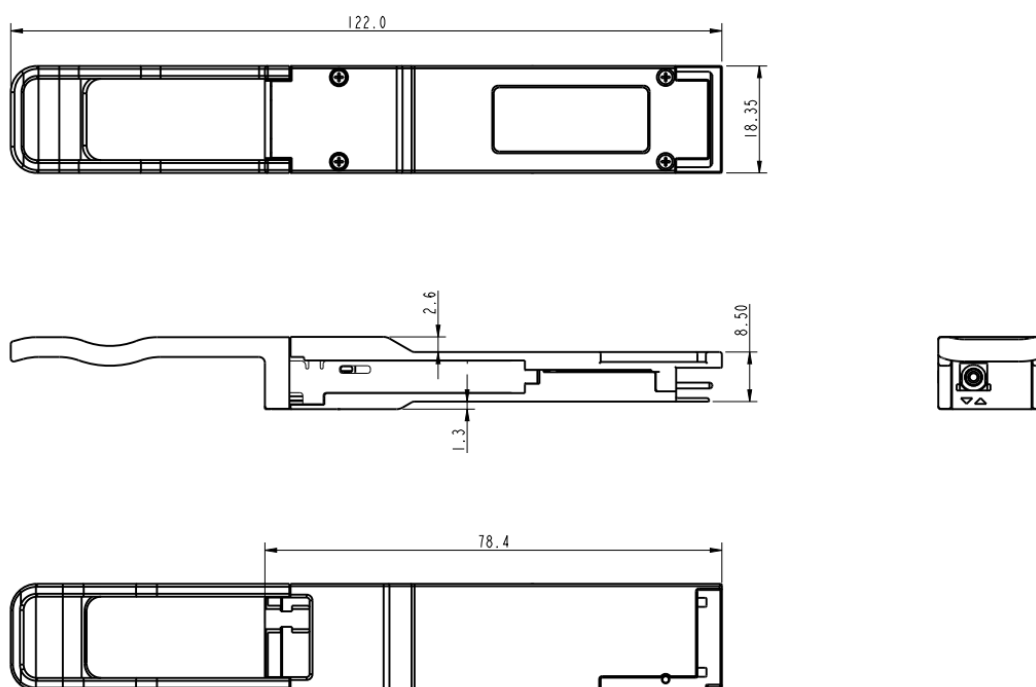
Figure 2-1 MSA compliant connector



3. Mechanical Specifications

Hisilicon's OM9382ES101&OM9382ES102 ER QSFP28 transceivers are compatible with the QSFP28 Specification for pluggable form factor modules.

Figure 3-1 Mechanical dimensions



4. Ordering Information

Part Number	Description
OM9382ES101	QSFP28 50GBASE-BR40
OM9382ES102	QSFP28 50GBASE-BR40

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